

[illegible]

	Date	Exmr.
<ul style="list-style-type: none"> - digital PLL phase freq det. - prot #3 head 100p f.ftr - ASIC & PLL - weighted memory - digital clock & <ul style="list-style-type: none"> compensate interpolator filter - dropy B <ul style="list-style-type: none"> stop response predict, anticipate 	8/8/83	pm

[illegible]